

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Receipt #4

In re the Application of

Laurence R. Simar, Jr., et al.

Docket Number: TI-30559

Serial No.: 09/702,320

Art Unit: 2183

Filed: 10/31/00

Examiner: Not yet assigned

For: MICROPROCESSOR WITH EXECUTION PACKET  
SPANNING TWO OR MORE FETCH PACKETS

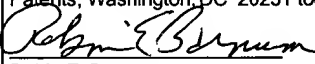
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REQUEST TO CORRECT FILING RECEIPT

Assistant Commissioner  
for Patents  
Washington, DC 20231

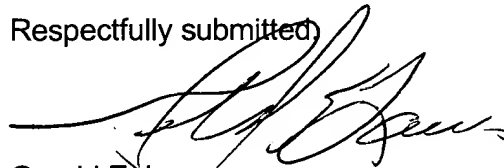
Dear Sir:

MAILING CERTIFICATE	
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231 today.	
 Robin E. Barnum	<u>8/29/01</u> Date

Enclosed is a photocopy of the Filing Receipt (PTO-103X) received from the United States Patent and Trademark Office in the above-identified application. The filing receipt is erroneous since it does not show priority under 35 U.S.C. § 119(e)(1) of provisional application number 60/165,512 filed 11/15/99, of provisional application number 60/183,527 filed 2/18/00 and of provisional application number 60/183,609 filed 2/18/00. Priority was requested as reflected on the enclosed first page of the specification mailed in with the non-provisional application.

Applicants respectfully request a corrected Filing Receipt (Form PTO-103X) and believe that Applicants did not cause this error and that no fee is due. However, this letter authorizes any necessary charges to Deposit Account No. 20-0668 of Texas Instruments.

Respectfully submitted,



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Registration No. 39,268  
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UNITED STATES PATENT AND TRADEMARK OFFICE  
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/702,320	10/31/2000	2183	840	TI-30559	8	7	2

23494  
TEXAS INSTRUMENTS INCORPORATED  
P O BOX 655474, M/S 3999  
DALLAS, TX 75265

**FILING RECEIPT**



\*OC000000005759933\*

Date Mailed: 02/13/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the PTO processes the reply to the Notice, the PTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

**Applicant(s)**

Laurence R. Simar, Jr., Richmond, TX ;  
Richard A. Brown, Houston, TX ;

**Continuing Data as Claimed by Applicant**

Provisional Application Nos. 60/165,512 11/15/99  
60/183,527 2/18/00  
Foreign Applications 60/183,609 2/18/00

If Required, Foreign Filing License Granted 01/11/2001

**Title**

Microprocessor with execution packet spanning two or more fetch packets

**Preliminary Class**

712

Data entry by : JONES, ANGELONA

Team : OIPE

Date: 02/13/2001



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# MICROPROCESSOR WITH EXECUTION PACKET SPANNING TWO OR MORE FETCH PACKETS

Laurence Simar

Richard A. Brown

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This application claims priority under 35 USC §119(e)(1) of Provisional Application No. 60/165,512, filed November 15, 1999 (TI-29909PS), of Provisional Application No. 60/183,527, filed February 18, 2000 (TI-30302PS), and of Provisional Application No. 60/183,609, filed February 18, 2000 (TI-30559PS).

## Technical Field of the Invention

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This invention relates to data processing devices, electronic processing and control systems and methods of their manufacture and operation, and particularly relates to memory access schemes of microprocessors optimized for digital signal processing.

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Bib Data Sheet

CONFIRMATION NO. 9784

<b>SERIAL NUMBER</b> 09/702,320	<b>FILING DATE</b> 10/31/2000 <b>RULE</b>	<b>CLASS</b> 712	<b>GROUP ART UNIT</b> 2183	<b>ATTORNEY DOCKET NO.</b> TI-30559	
<b>APPLICANTS</b> Laurence R. Simar, Jr., Richmond, TX; Richard A. Brown, Houston, TX;					
<b>** CONTINUING DATA *****</b> THIS APPLN CLAIMS BENEFIT OF 60/165,512 11/15/1999 <i>DSH</i> WHICH CLAIMS BENEFIT OF 60/183,527 02/18/2000 WHICH CLAIMS BENEFIT OF 60/183,609 02/18/2000					
<b>** FOREIGN APPLICATIONS *****</b>					
<b>IF REQUIRED, FOREIGN FILING LICENSE GRANTED</b> <b>** 01/11/2001</b>					
Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after met <i>Allowance</i> Verified and Acknowledged <i>Don't F. Brown</i> <i>DSH</i> Examiner's Signature Initials		<b>STATE OR COUNTRY</b> TX	<b>SHEETS DRAWING</b> 8	<b>TOTAL CLAIMS</b> 7	<b>INDEPENDENT CLAIMS</b> 2
<b>ADDRESS</b> 23494					
<b>TITLE</b> Microprocessor with execution packet spanning two or more fetch packets					
<b>FILING FEE RECEIVED</b> 840	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		